

CALL FOR PARTICIPATION

The 7th Annual International Conference on HIGH PERFORMANCE COMPUTING

HiPC 2000
Bangalore, India
December 17–20, 2000

www.hipc.org

This is the 7th annual International Conference on High Performance Computing organized in India. Papers are being solicited on all aspects of high performance computing, including (but not limited to) the following:

- Parallel Algorithms
- Parallel Architectures
- Parallel Languages and Compilers
- Distributed Systems
- Supercomputing Applications
- Signal and Image Processing Systems
- Internet and WWW-based Computing
- Scalable Servers
- Scientific Computation
- Visualization
- Network and Cluster-based Computing
- Memory Systems
- Embedded Systems
- Programming Environments
- Multimedia and High Speed Networks
- Wireless Mobile Computing

Special Sessions planned for HiPC 2000 include:

- Applied Parallel Processing
- Large-Scale Data Mining
- Compilation Techniques for Embedded Computing
- Cluster Computing and Its Applications
- High Performance Middleware
- Modeling and Analysis of Mobile Telecom Networks

General Co-Chairs:

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To submit an original research paper, send your complete manuscript (not to exceed 15 double-spaced pages of text using 12 point size type on 8.5 x 11 inch pages) to the Program Chair. References, figures, tables, etc. may be included in addition to the fifteen pages of text. Electronic submissions are strongly encouraged and should be emailed to hipc2000@ac.upc.es. Electronic submission details are available on the conference website and by e-mail. Hard copy submissions are permitted and, like electronic submissions, must be received by the deadline. Please send six copies of the manuscript to the Program Chair. Fax submissions will not be considered. All manuscripts will be reviewed. Conference proceedings will be published in the Springer-Verlag Lecture Notes in Computer Science (LNCS) series.

Proposals are solicited for tutorials to be held at the conference. Interested individuals should submit a proposal by the deadline to the Tutorials Chair. The proposal should include a brief description of the intended audience, a lecture outline, and vita for lecturer(s).



In addition to parallel sessions of contributed papers, a plenary poster/ presentation session emphasizing novel applications of high performance computing is planned. It will offer brief presentation time for each poster and will be followed by a walk-up and talk setting. To be considered, please send a 5 page summary of your work to the Poster/Presentation Chair by the deadline.

Important Dates

Conference Paper Due	:	May 1
Tutorial Paper Due	:	May 15
Notification of Acceptance/Rejection	:	Jun 30
Camera-Ready Paper Due	:	Aug 15
Poster/Presentation Summary Due	:	Oct 2

This and other conference information is available on the World Wide Web at <http://www.hipc.org>

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